

FIG. 1 A

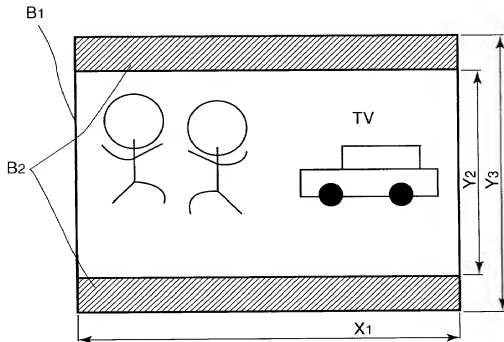
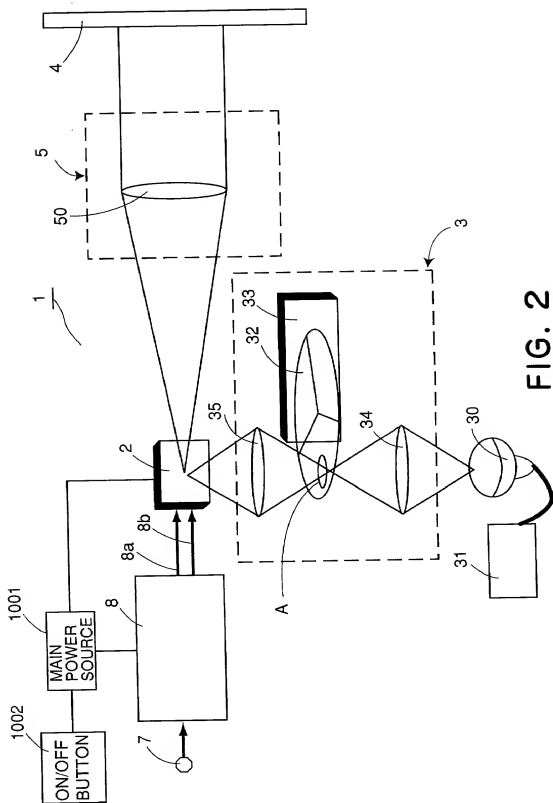


FIG. 1 B



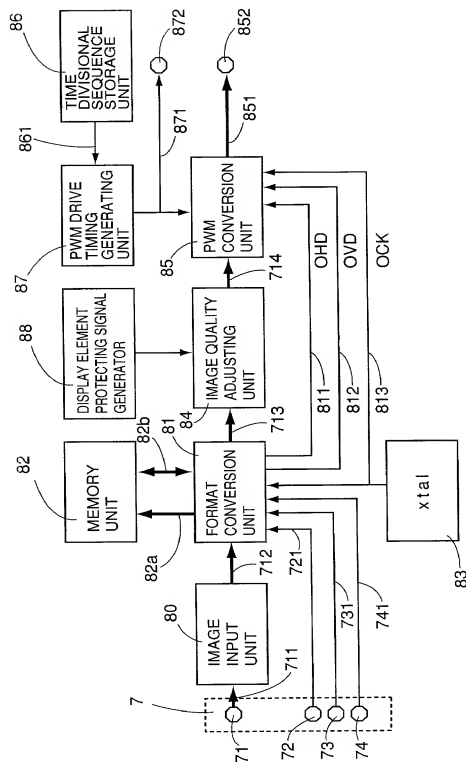


FIG. 3

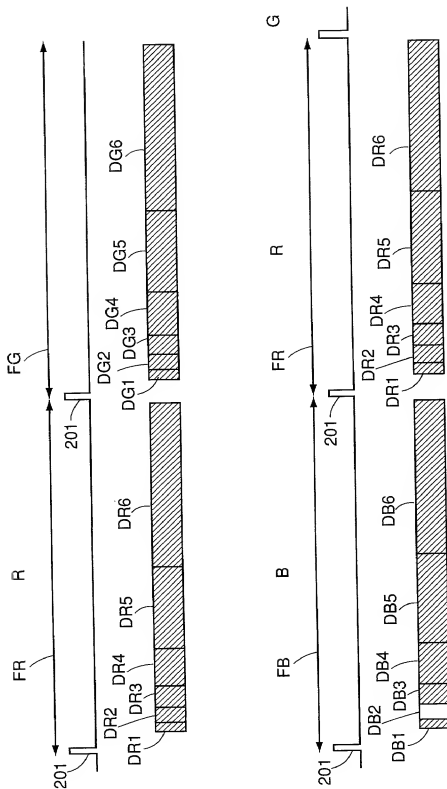


FIG. 4



FIG. 5

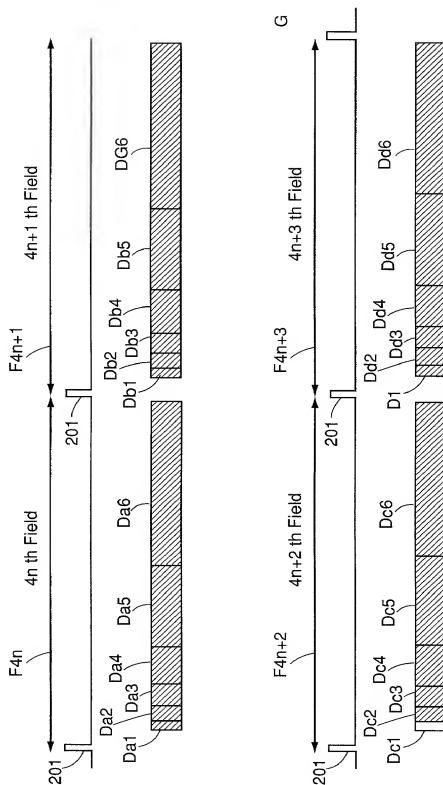


FIG. 6

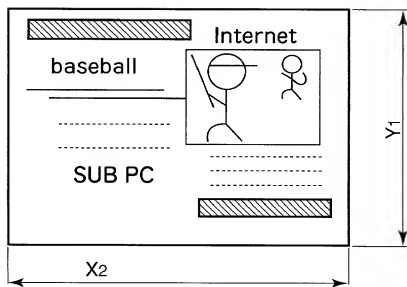


FIG. 7A

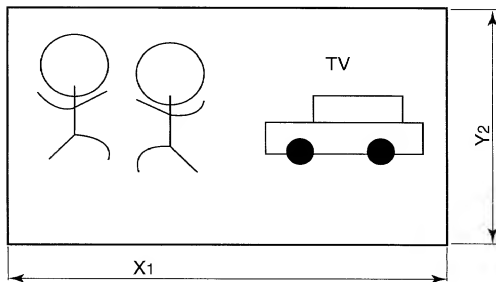


FIG. 7B

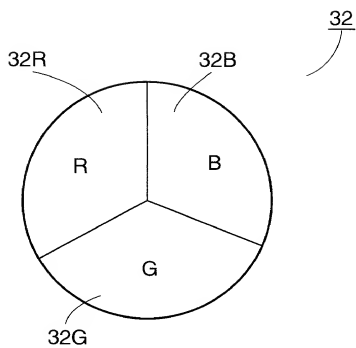


FIG. 8



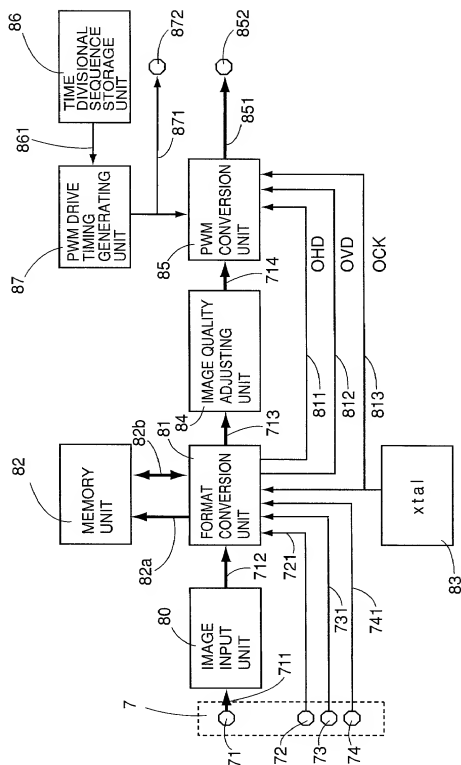


FIG. 9

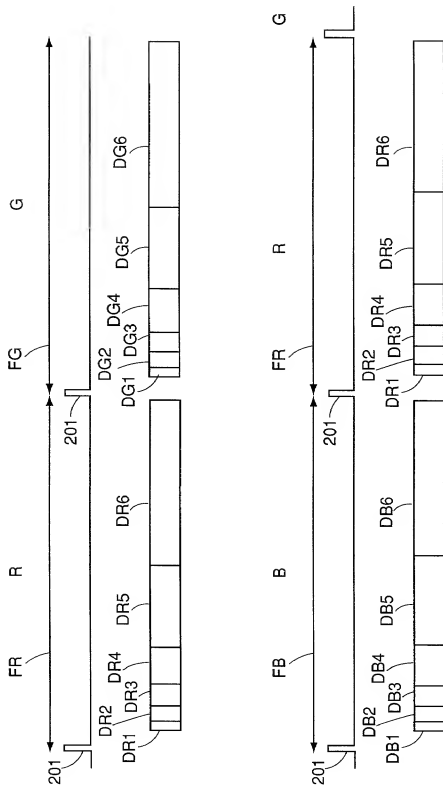


FIG. 10

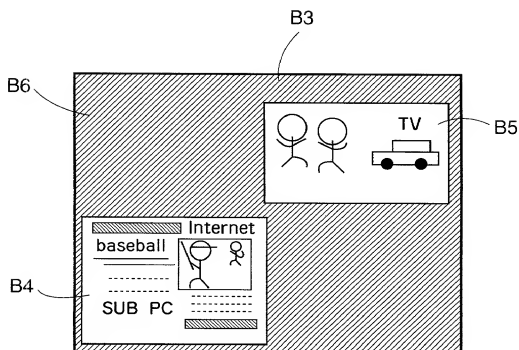


FIG.11

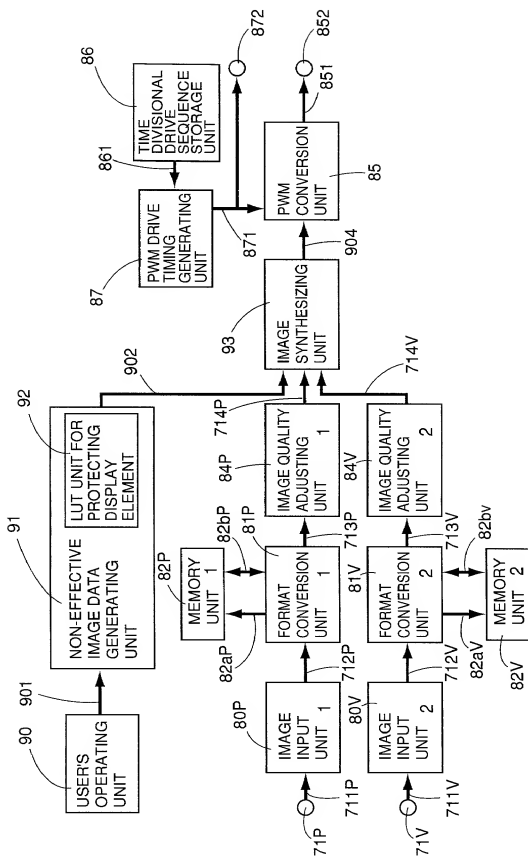


FIG. 12

INPUT VALUE	OUTPUT VALUE
0	0
1	1
2	2
3	3
4	4
5	5
6	6
.	.
.	.
.	.
58	58
59	59
60	60
61	60
62	60
63	60

FIG. 13A

INPUT VALUE	OUTPUT VALUE
0	0
1	1
2	2
3	3
4	4
5	5
6	6
.	.
.	.
.	.
58	58
59	59
60	60
61	61
62	62
63	62

FIG. 13B

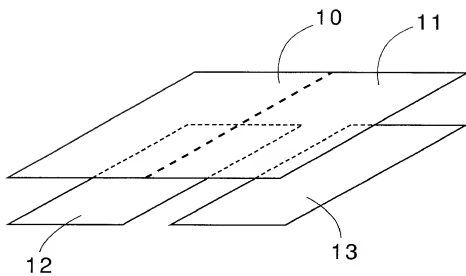


FIG. 14

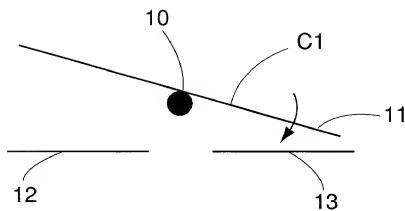


FIG. 15A

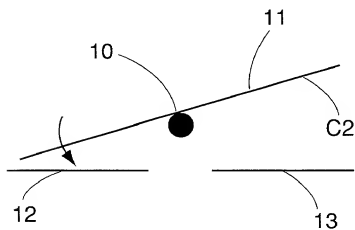


FIG. 15B

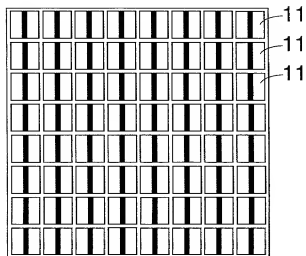


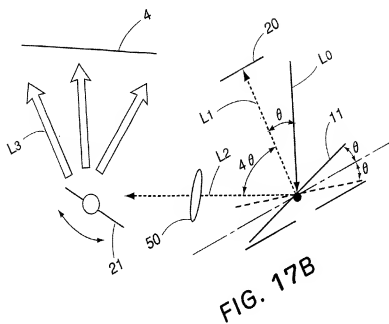
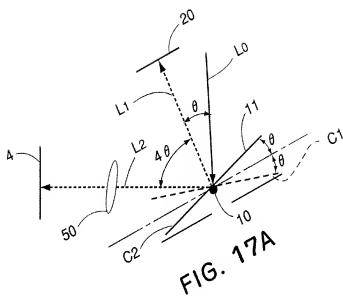
FIG. 16A



FIG. 16B



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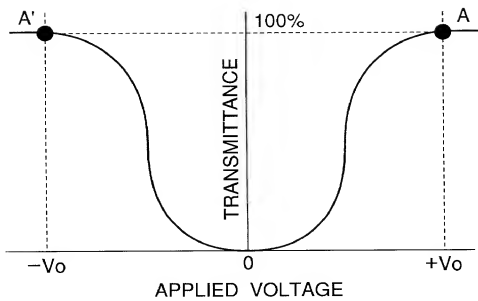


FIG. 19

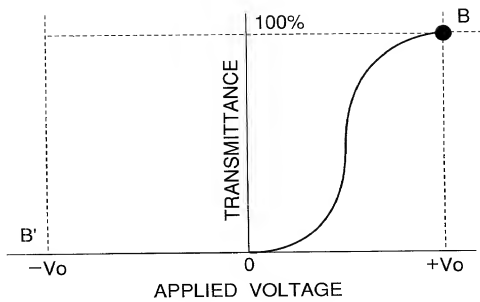


FIG. 20

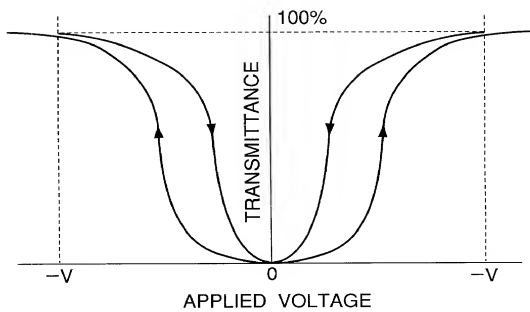


FIG. 21

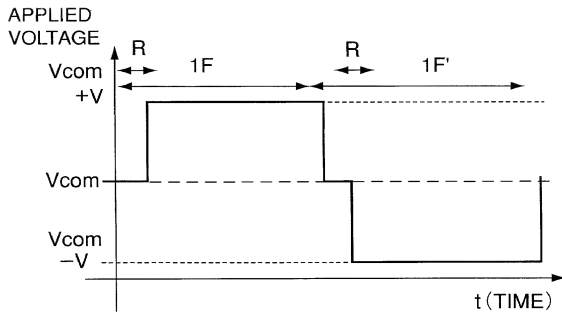


FIG. 22